

## Monolayer Resist for Patterned Contact Printing of Aligned Nanowire Arrays

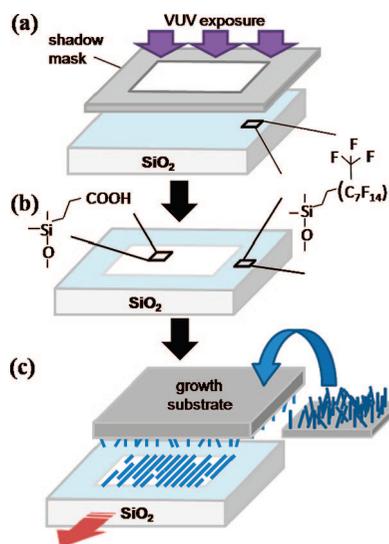
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In recent years, single crystalline, semiconductor nanowires (NWs) have been widely explored as the potential building blocks for various technological applications, such as electronics,<sup>1</sup> photonics,<sup>2</sup> sensors,<sup>3</sup> and energy conversions.<sup>4</sup> To fully extend the potential, however, the development of generic assembly methods is required.<sup>5</sup> Recently, we demonstrated a simple contact printing approach for the large-scale assembly of parallel arrays of NWs on rigid and mechanically flexible substrates.<sup>6</sup> During this process, direct and aligned transfer of NWs from the growth (i.e., donor) substrate to the receiver substrate is attained. This highly versatile approach is attractive for large-area printable electronics with the printed NW parallel arrays being configured as the channel or active material for transistors and sensors. Previously, the NW patterning was achieved by photolithographically patterning a polymeric resist layer on the receiver substrate prior to the printing process.<sup>6</sup> Following the printing process, the resist layer covered with the printed NWs is dissolved in a solvent, leaving behind only the NWs assembled at predefined locations. To further demonstrate the versatility of the contact printing process, here, we report patterned printing of NWs by using fluorinated self-assembled monolayers (SAMs) as the resist layer. By projecting a light pattern on the surface of the SAM resist in an oxygen-rich environment, sticky and nonsticky regions on the surface are directly defined in a single-step process which then enables the highly specific and patterned transfer of the NWs by the printing process, without the need for a subsequent lift-off step. This work demonstrates a novel route toward scalable, patterned printing of NWs on substrates by utilizing very ultraviolet (VUV) tunable, nanoscale chemical interactions.

The process flow for the patterned NW printing with an SAM resist is illustrated in Figure 1. First, a Si/SiO<sub>2</sub> (50 nm, thermally grown) substrate is chemically reacted with (heptadecafluoro-1,1,2,2-tetrahydrodecyl)dimethylchlorosilane (HDF) to form a highly stable, fluorinated SAM on the surface of the substrate. A light pattern is then projected on the SAM resist by using an excimer light source (172 nm, ~25 mW/cm<sup>2</sup>) and a shadow mask (Figure 1a). The VUV light exposure in the presence of O<sub>2</sub> results in the chemical cleavage of the F–C bonds and the formation of –COOH and –CHO functional end groups (Figure 1b).<sup>7</sup> While the original fluorinated SAM is highly nonsticky to the NWs, the VUV-modified monolayer exhibits strong binding interactions with the NWs, most likely through H-bonding. Following the patterned exposure of the SAM resist, the NW printing is conducted by directionally sliding a growth substrate consisting of a dense forest of Ge or Si NWs (grown nonepitaxially by vapor–liquid–solid process) on top of the receiver substrate. A lubricant (octane: mineral oil, 2:1 v/v) is applied between the two substrates to minimize the mechanical interactions, enhancing the dominant role of the well-controlled chemical interactions in guiding the NW transfer process. During the printing process, NWs are dragged on the receiver substrate, resulting in their alignment in the direction of the shear force

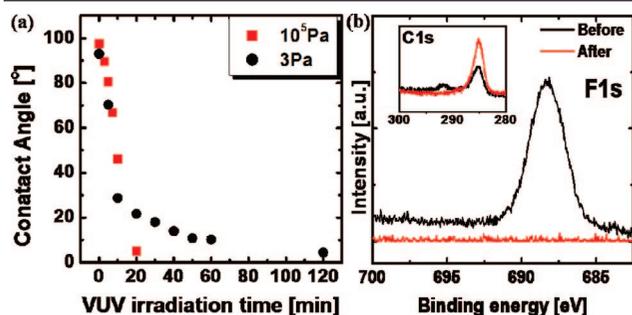


**Figure 1.** Schematic of the patterned contact printing process with monolayer resist. (a) Patterning of fluorinated SiO<sub>2</sub> surface is carried out by VUV irradiation in the presence of O<sub>2</sub>. (b) C–F bonds in the exposed regions are cleaved and replaced with –COOH or –CHO functional groups. (c) Contact printing results in the aligned transfer of NWs on the VUV exposed regions.

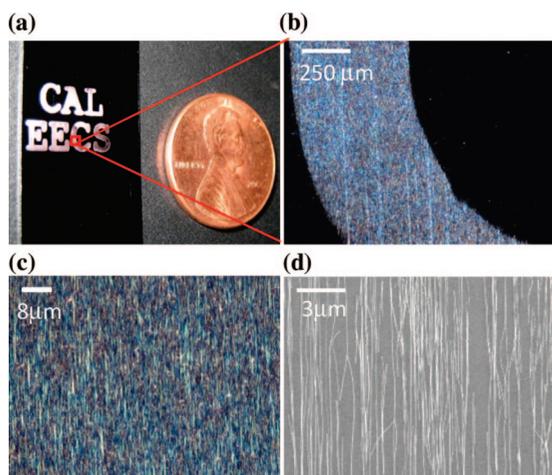
(Supporting Information). Once anchored to the surface of the receiver substrate by chemical binding interactions, NWs are detached from the growth substrate. As a result, highly aligned NW arrays are assembled on the exposed SAM regions of the receiver substrate while no NWs are assembled on the unexposed, fluorinated SAM regions (Figure 1c).

To characterize the properties of the VUV exposed SAMs, contact angle, XPS, and ellipsometry measurements were performed as a function of the exposure time. As shown in Figure 2a, the contact angle of the HDF treated surface is ~100°, showing a highly hydrophobic surface property. As the monolayer is exposed to VUV, however, the contact angle decreases and the surface changes to possess a hydrophilic property. Specifically, the contact angle is reduced to ~10° after 60 min of exposure in 3 Pa O<sub>2</sub>/N<sub>2</sub> (21, 79%) ambient. On the other hand, when irradiation is carried out at 10<sup>5</sup> Pa, it takes <20 min to achieve a similar contact angle. This accelerated chemical transformation of the monolayer by increasing the pressure depicts the important role of O<sub>2</sub> in the VUV enabled surface chemical modification.<sup>7</sup>

From XPS measurements (Figure 2b), the VUV-induced, chemical modification of the monolayer is directly confirmed. While the F 1s peak at 688 eV, originating from C–F bonds, is clearly observed for the unexposed SAM, it is completely diminished after the VUV irradiation. This is also seen from the C 1s spectrum, where the C–F peak at 292 eV disappears upon VUV irradiation.



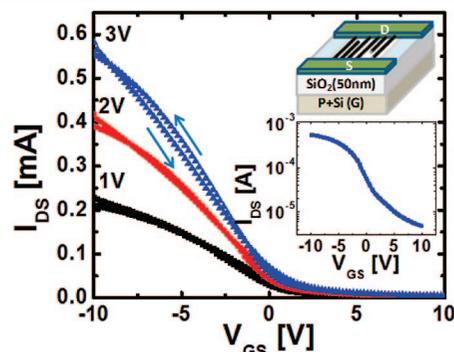
**Figure 2.** (a) Water contact angle as a function of VUV irradiation time. Irradiation is carried out at  $10^5$  Pa and 3 Pa, corresponding to  $2 \times 10^4$  Pa and 0.6 Pa of oxygen partial pressure, respectively. (b) F1s and C1s (inset) XPS spectra of fluorinated surfaces before (black curve) and after (red curve) VUV irradiation for 30 min at  $10^5$  Pa.



**Figure 3.** (a) Optical bright field, (b–c) optical dark field, and (d) SEM images of Ge NWs ( $d \sim 30$  nm) printed on the VUV patterned SAM-resist.

On the hand, the C 1s peak at  $\sim 285$  eV which corresponds to the C–C bond remains intact, even after the VUV exposure. The results suggest that the alkyl backbone of the monolayer remains intact with only the C–F bonds being oxidized upon VUV irradiation and replaced with polar –COOH and –CHO species. This is also confirmed from ellipsometry measurements where SAM thicknesses of  $\sim 1.1$  and  $0.9$  nm are observed for the unexposed and exposed SAMs, respectively. The observed results are consistent with the previously reported VUV modification of fluorinated SAMs on  $\text{SiO}_2$  surfaces in the presence of  $\text{O}_2$ .<sup>7</sup> In those studies, it was proposed that  $\text{O}_2$  forms highly active species upon VUV irradiation which then readily oxidize and cleave the C–F bonds. A similar surface chemical reaction is expected in our fluorinated SAMs.

Optical and scanning electron microscopy images of printed Ge NWs ( $d \sim 30$  nm) on the VUV-patterned fluorinated  $\text{SiO}_2/\text{Si}$  substrate are shown in Figure 3. Fluorinated regions possess highly nonsticky surface properties; therefore, preventing NW transfer during the contact printing process. On the other hand, NW parallel arrays are readily assembled on the hydrophilic regions, enabled by the VUV exposure. The transferred NWs are found to be relatively dense ( $2\text{--}5$  NW/ $\mu\text{m}$ ) and well aligned (Figure 3d). The density may be further enhanced in the future by using complementary surface modification of VUV-irradiated regions, which was not incorporated in this work. As clearly evident, this simple process enables a high degree of selectivity in the assembly of the NWs in well-defined locations on surfaces, especially for applications that do not require nm-scale positioning, such as for large-area electronics with the device dimensions on the order of tens



**Figure 4.** Transfer characteristics of a parallel-array Si NW FET with  $W \sim 200$   $\mu\text{m}$  and  $L \sim 3$   $\mu\text{m}$  (Insert shows log scale,  $V_{\text{ds}} = 3$  V).

of microns. In our current setup, a resolution limit of  $\sim 5$   $\mu\text{m}$  is obtained which is limited by the nonideal contact between the shadow mask and the substrate.

To further investigate the feasibility of this printing approach for electronic applications, we fabricated back-gated (gate oxide thickness  $\sim 50$  nm  $\text{SiO}_2$ ), field-effect transistors (FETs) with channel width  $\sim 200$   $\mu\text{m}$  and source/drain (S/D) spacing  $\sim 3$   $\mu\text{m}$  by using  $p$ -type Si NWs ( $d \sim 30$  nm, B doped). Notably, the assembled NWs do not detach from the substrate during the standard microfabrication processing steps used here (Supporting Information). Figure 4 shows a representative transfer characteristic of such a proof-of-concept device structure. The transistor delivers a modest ON current of  $\sim 0.6$  mA at  $V_{\text{DS}} = 3$  V with  $I_{\text{ON}}/I_{\text{OFF}} > 100$ . This ON current corresponds to  $\sim 1$   $\mu\text{A}/\mu\text{m}$  assuming a NW density of 3 NW/ $\mu\text{m}$ , which is consistent with the observed  $I_{\text{ON}}$  for a typical single Si NW FET configured in a similar device geometry.

In summary, well-defined and highly specific NW assembly is achieved by combining contact printing with direct SAM patterning on  $\text{SiO}_2$  surfaces. Here, the SAM is effectively used as the resist for the NW transfer process, enabling a highly versatile, one-step process for achieving a patterned assembly of parallel arrays of NWs on substrates.

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**Supporting Information Available:** Nanowire synthesis details, surface modification, and contact printing procedure. This material is available free of charge via the Internet at <http://pubs.acs.org>.

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## Supporting Information

### ***NANOWIRE GROWTH***

Si and Ge NWs were synthesized by gold nanoparticle mediated vapor-liquid-solid process. Monodispersed gold nanoparticles with diameter,  $d=30$  nm (Ted Pella, Inc.) were deposited on Si/SiO<sub>2</sub> (50nm, thermally grown) substrates, followed by the chemical vapor deposition growth. *p*-type SiNWs were grown at 460 °C and 30 torr under a constant flow of silane (4 s.c.c.m), hydrogen (4 s.c.c.m), and diborane (0.01 % diluted in H<sub>2</sub>, 5 s.c.c.m) for 15 minutes, resulting ~120 μm long NWs. For *p*-type Ge NWs, germane (10% diluted in H<sub>2</sub>, 10 s.c.c.m.), diborane (0.01 % diluted in H<sub>2</sub>, 5 s.c.c.m), and H<sub>2</sub> (90 s.c.c.m) were used at 270 °C and 700 torr, resulting in a growth rate of ~ 1 μm/min.

### ***SURFACE FUNCTIONALIZATION OF THE RECIEVER SUBSTRATES***

Fluorination of SiO<sub>2</sub> surfaces was carried out by the well established siloxane-based condensation chemistry using (heptadecafluoro-1,1,2,2-tetrahydrodecyl) dimethylchlorosilane (Gelest, Inc.). A solution of ~0.5 vol% of the above compound was prepared with hexane as the solvent. Si/SiO<sub>2</sub> substrates were then treated in the solution for 45 min, followed by a thorough wash with the solvent and baking in an oven at 120°C for 20 min, resulting in a highly uniform fluorinated monolayer on the SiO<sub>2</sub> surface.

### ***MONOLAYER PATTERNING WITH VERY ULTRAVIOLET (VUV) EXPOSURE***

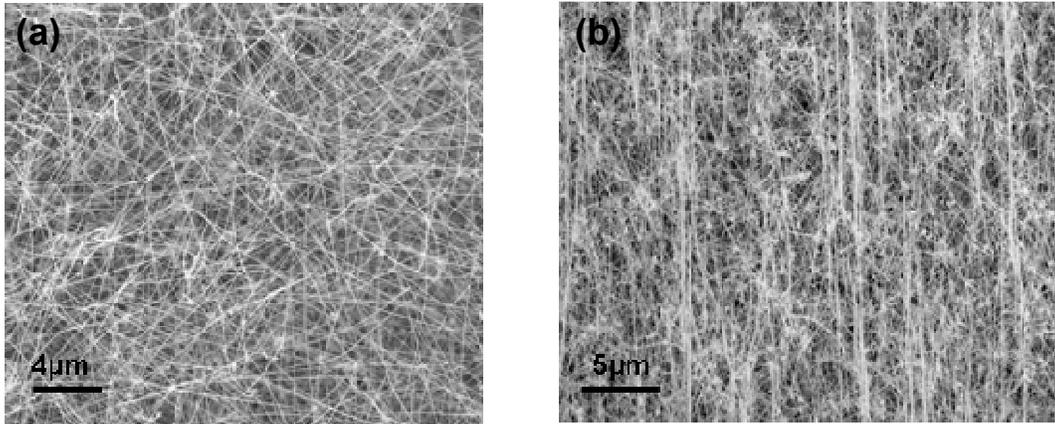
We used a Xe-filled dielectric barrier discharge excimer lamp with an emission wavelength of 172 nm as the light source to pattern the above described fluorinated monolayer. The samples were irradiated with VUV through a shadow mask in contact with the sample surface. During the irradiation, the chamber was kept at 10<sup>5</sup> or 3 Pa ambient air, corresponding to ~2×10<sup>4</sup> and 0.6 Pa

oxygen partial pressure, respectively. VUV exposure is extensively attenuated due to the absorbance of oxygen molecules in air, so the distance between the light source and sample surface was kept to be less than 5 mm.

### ***NANOWIRE PRINTING***

The surface modified receiver substrate was mounted on a fixed stage. The growth substrate (i.e. donor substrate) with a dense lawn of NWs was cut into the desired size and attached to a metal weight (chosen to result in a pressure of  $\sim 10 \text{ g/cm}^2$ ). After a few drops of lubricant (Octane: mineral oil, 2:1 v:v), the donor chip along with the weight was gently contacted with the receiver substrate. Donor chip was then pushed by a micromanipulator with a constant velocity of  $\sim 20 \text{ mm/min}$  in the direction of the VUV-irradiated patterns. After sliding for a few mm, a second weight was added to the donor chip to increase the total pressure to  $\sim 54 \text{ g/cm}^2$  while keeping the same velocity of  $\sim 20 \text{ mm/min}$ . This procedure enables us to avoid mechanical “stamping” of NWs when the donor is initially contacted with the receiver substrate. After the donor chip was passed over the entire receiver substrate, the donor chip was gently removed and the receiver substrate was rinsed with octane. SEM images of the donor substrate before and after the printing process is shown in Figure S1, clearly demonstrating that NWs on the donor substrate are effectively combed by the shear force, before getting transferred to the receiver substrate. Assuming a NW density of  $\sim 5 \text{ NW}/\mu\text{m}^2$  on the donor substrate and a density of  $\sim 50 \text{ NW}/100 \mu\text{m}^2$  on the receiver substrate, we estimate that only  $\sim 10\%$  of the NWs are transferred during the contact printing process when the donor and the receiver substrates have the same surface area. The printing process is generic for a wide range of receiver substrates, such as flexible Kapton,

glass, and Si/SiO<sub>2</sub>, as long as the surface roughness is on order of a few nm or less, and the appropriate surface chemistry is utilized.<sup>1</sup>

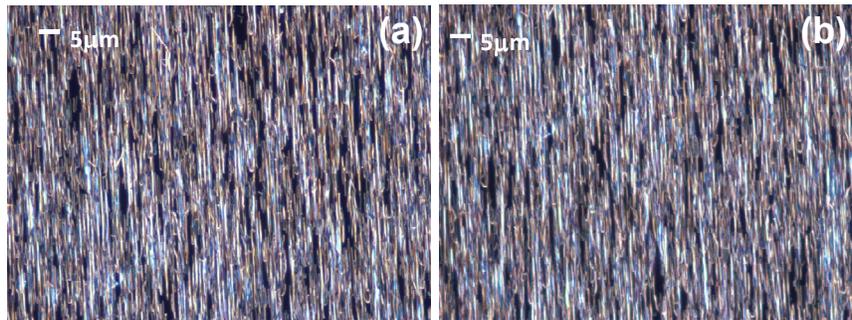


*Figure S1.* Top-view SEM images of the Ge NWs on the donor substrate (a) prior and (b) after the contact printing process.

### ***NANOWIRE ARRAY DEVICE FABRICATION AND CHARACTERIZATION***

For the electrical measurements, a heavily B doped Si substrate was used as a global back-gate. Source/drain electrodes were patterned with photolithography (Karl Suss MA6 Mask Aligner), followed by Co electron-beam evaporation (~50 nm thick), and lift-off. The spacing between source and drain electrodes is ~3 μm. The sample was then annealed at 300 °C for 30s. After this thermal treatment, the electrical transport properties of the devices were investigated with a probe station system (Signatone) and semiconductor parameter analyzer (Agilent 4155C) at room temperature. It should be noted that during the fabrication, we avoided using ultrasonication (for instance, during the lift-off steps) as doing so results in the gradual removal of the NWs from the substrate. By using the above fabrication procedure, the density of the assembled NWs (for the diameter range explored in this study) remains the same after the device fabrication steps, clearly indicating the strong adhesion of the NWs to the receiver substrate for

fabrication processing purposes. Additionally, we have done the simple Scotch tape test in which a removable tape is gently brought in contact to the surface of the substrate and then removed. We did not observe any noticeable change in the NW density after this test (Fig. S2).



*Figure S2.* Dark-field optical images of the assembled GeNW arrays (a) before and (b) after the Scotch tape test, showing no noticeable change in the density of the assembled NWs.

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